

## A Novel Bidirectional Soft Switching Dc-Dc Converter

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**Abstract:** The Bidirectional DC-DC (BDC) Converter topologies plays major role in HEV, RES based system etc., As the demand for BDC is increasing, it should be made more compact with high efficiency. Keeping this in view, this work proposed an efficient BDC using coupled Inductor with reduced switching losses. Further, to improve the dynamic performance of the converter, Fuzzy controller is integrated. The performance of the proposed topology is studied using MATLAB simulation. From the results, it is concluded that the proposed topology exhibits higher voltages gain with reduced switching loss.

**Keywords:** Bidirectional converter, Proportional Integral Controller, coupled inductor.

### 1. Introduction

Increase in demand for fuel and environmental protection control have made it necessary to move towards the Renewable Energy Sources (RES). This leads to the transition of internal combustion engine (ICE) vehicles to electric vehicles (EVs) or hybrid electric vehicles (HEVs) is very attractive and desirable, but there are still some serious issues with regard to energy storage technology. In these applications, the combination of batteries and ultra- capacitors are used as an energy storage unit. The battery is connected with the ultra-capacitor through a bidirectional dc/dc converter [1-3]. Thus the designed bidirectional DC-DC converter should accomplish a high power density with low current/voltage ripple. At the same time, the converter should have high efficiency, low cost, low EMI, and compact component size. However, the converter efficiency is reduced at high frequencies due to switching losses. The switching loss can be reduced by soft switching. This soft switching eliminates the losses by limiting the current or voltage at the switching moment and improves efficiency of the converter [4-10].

Hence, this work proposed a new bidirectional converter with reduced switching loss. Thus the proposed converter comprises a half bridge topology with coupled inductor. In this work, the steady state analysis of the proposed topology is explained. From the analysis, it is proven that that the proposed converters possess increased efficiency then the conventional converter.

However, a BDC must provide a regulated DC output voltage under varying input / load condition. Hence, to improve the dynamic performance of the system, this work proposed a robust AI controller namely fuzzy to control the proposed BDC.

### 2. Proposed Bidirectional Converter

Fig. 2 depicts circuit diagram of the proposed converter. The auxiliary circuit of this BDC comprises coupled inductor and resonant capacitors. This will carry out ZVS condition and hence, the ripple present in the inductor current cancels automatically irrespective of the direction of power flow [11, 12].

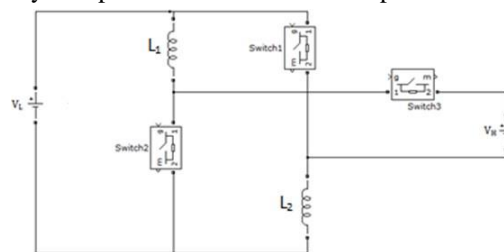


Fig.2 Proposed DC-DC Boost/Buck converter

Proposed converter has two operation modes.

- ✓ Boost mode
- ✓ Buck mode.

Boost Mode of Operation

During boost mode, switch  $S_2$  remain off and  $S_1$  turns on.

Buck Mode of Operation

In step down, Condition of switches are interchanged.

### 3. Result And Discussion

To validate the performance of the proposed converter, simulations were carried in MATLAB environment and the obtained results are discussed below.

## Open Loop Analysis

### i. Boost Mode:

Under open loop control, a step change of 5V is applied at the input as shown in Fig.3a.

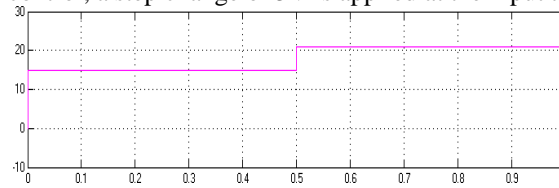


Fig. 3a : Input Voltage

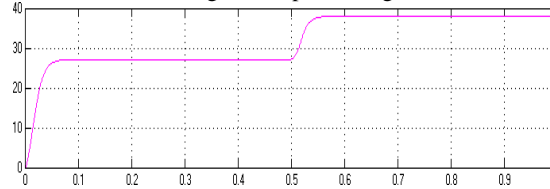


Fig. 3b: Output Voltage

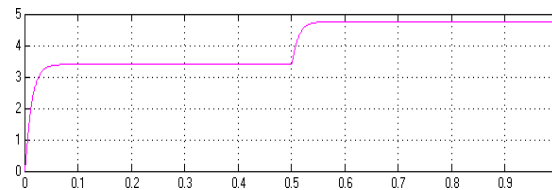


Fig. 3c: Output Current

**Fig 3. Analysis under Boost mode of operation**

Thus, the obtained output voltage and current during boost operation is depicted in figure 3b and 3c. Similarly, the open loop controlled DC-DC converter operating in buck mode is shown in Fig.4. The variation of input voltage, output voltage and output power are shown in figures 4a, 4b & 4c.

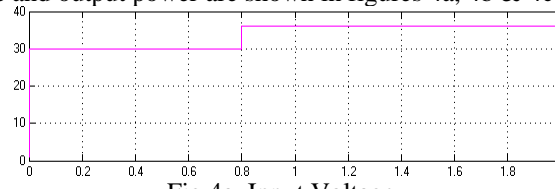


Fig.4a. Input Voltage

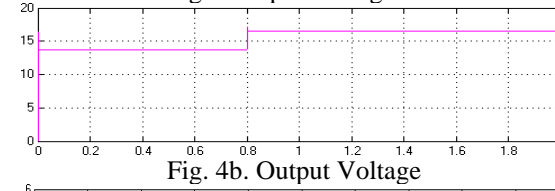


Fig. 4b. Output Voltage

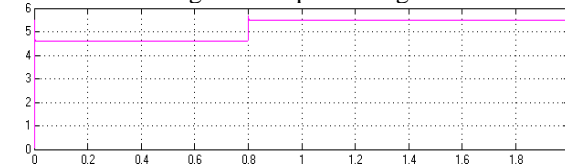


Fig. 4c. Output Current

**Fig 4. Analysis under Buck mode of operation**

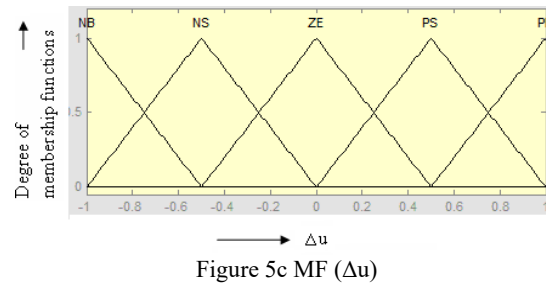
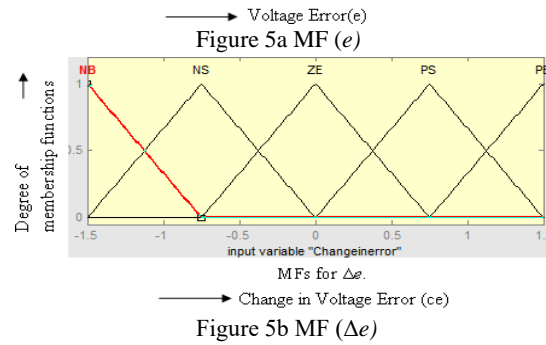
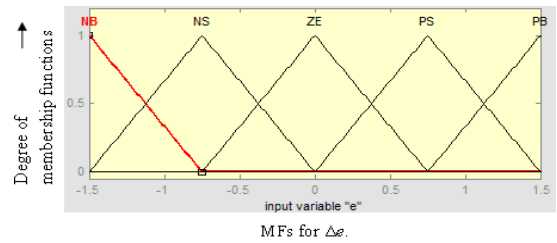
### Analysis Under Closed Loop Control

In order to enhance the performance of the proposed converter, controllers are implemented. In this topology, Fuzzy controller is adopted for as a controller for proposed BDC.

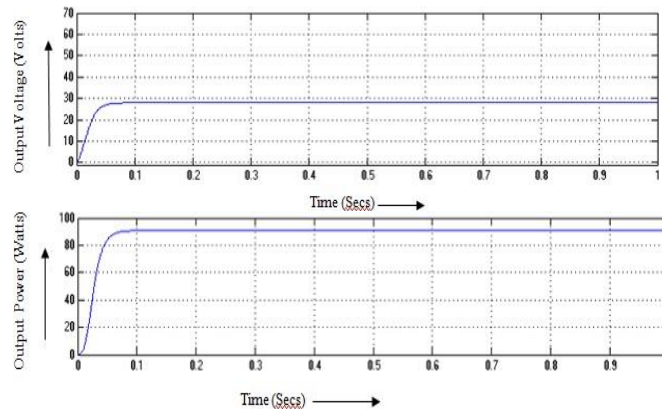
Thus, the designed FLC is characterized with

- 5 Triangular MFs using mamdani fuzzy reasoning method.
- Centroid for defuzzification process.

Thus, the output obtained from the fuzzy controller controls the modulating pulses applied to the switches of the converter. Thus, the designed MF for proposed FLC is depicted in fig. 5

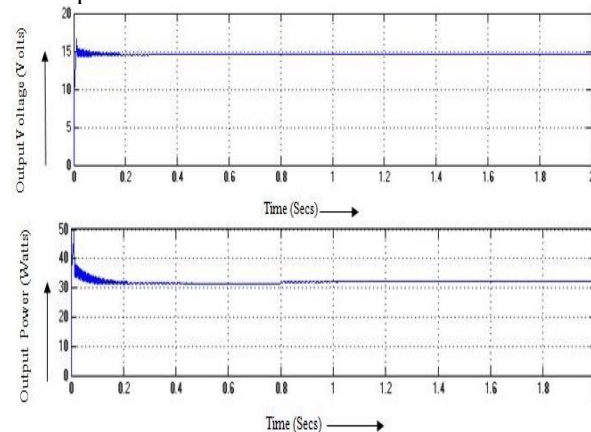


**Fig 5. Designed MF of proposed FLC**



**Fig. 6. Analysis of proposed BDC with FLC (Boost Mode)**

Figure 5 depicts the output voltage and of the BDC with FLC controller. From the waveform, it is concluded that the proposed FLC controller regulates the output without any overshoot and the time taken for its steady state response is also less when compared to conventional controller.



**Fig.7 Analysis of proposed BDC with FLC (Buck Mode)**

Similarly, the output obtained during buck mode is depicted in figure 7. In the buck mode also, the controller proved its efficiency. Hence, it is concluded that proposed FLC enhances the dynamic performance of the proposed efficiency.

**Comparative analysis**

Table 1 Measured efficiency in the proposed converter and conventional Bidirectional boost/buck converter.

Converter Type	Mode	Efficiency
Conventional	Boost	90.7%
	Buck	86%
Proposed	Boost	93%
	Buck	90%

Thus the efficiency of the proposed BDC with conventional one is tabulated in Table 1. From the table, it is proven that the proposed BDC exhibits 93% and 90% in boost and buck mode which is higher than that of the conventional one. Hence, it is proven that the proposed converter shows higher efficiency in all modes of operation with less switching loss.

**4. Conclusion**

A BDC suitable for EV application is designed in this work. It comprises half bridge topology with coupled inductor. In this topology, as ZVS switching is obtained, the loss due to switching gets reduced. similarly, when compared to conventional topology, the proposed controller exhibits higher efficiency. Finally to enhance to dynamic behaviour of BDC, FLC is implemented. From the results, it is evident that this proposed BDC is suitable for EV application.

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