Research Article

Inverter Current Feedback Control Of A Multilevel Inverter Controlling An Induction Motor Drive

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Article History: Received: 10 January 2021; Revised: 12 February 2021; Accepted: 27 March 2021; Published online: 16 April 2021

Abstract: An attempt is made to control the current of a PV integrated three-phase Cascaded H Bridge inverter, connected to a three-phase load with a matrix converter. The controller is designed to control the current and maintain the load power constant at any point of the disturbance enabling suitable control methods like PR controller {using the concept of inverter-current feedback (ICF)} and PI controller. A Matlab Simulink model of the said system is developed with suitable switching angles of the inverter switch selected using Differential Evolutional Algorithms (DEA). All the converters are modeled using a small scale averaging method to tune the controller. A novel controller technique of Matrix converter based multilevel inverter is proposed with the Proportional Resonant (PR) controller to control the power and the results are compared with the PI regulator. The ICF helps the PR controller to adjust the reference current and generate suitable control signal which is useful in reducing the harmonic content of the signal and hence a less distorted signal is produced during transients.

Keywords: CHB-MLI, Matrix converter, Proportional Resonant controller, ICF, THD, DEA.

I. INTRODUCTION

A Multilevel Inverter (MLI) is empowered to generate enormous power with a coordinated renewable energy sources having minimum dv/dt stress [1]. Power system optimization and a suitable range of the desirable controlling factors maintain a balance among the cost, power discrepancy and reliability with appropriate filtration methods. For power system estimation & grid integration, an optimum inverter design and power quality can be the constraints of trade-off. It is essential to evaluate the system performance, fidelity of the device and system prudential constraints when maintaining the power quality. In the field of cyclo converters, the matrix converters play a significant role in developing desired frequency output irrespective of the input. By employing the proper modulation technique, [10] the input-side power factor can be improved, which can eliminate the requirement of power factor correction devices at the cost of an increased number of switches. A tradeoff is always desirable when selecting control between the converter components and the conventional system. [14] The ripples generated due to switching frequencies needs to get the filter out with the help of the LCL filter. As the increased value of filter inductance provides high attenuation along with increased size of the filter [1, 3], it is preferred to implement control methods like (inverter-current feedback (ICF) or load-current feedback (LCF)) [8] to compensate for the low-order harmonics else a bulkier filter inductance can be a consequence. In ICF control the current sensors are accumulated in the inverter side for overcurrent surge and also supply an inherent damping characteristic to ensure the stability of the system under disturbed condition.

Many researchers have published articles to develop new concepts in these areas. Some of them are given below;

The THD reduction is justified by the increased number of levels in [1, 4] and by the comparison of the generation of delay angle by different modulation methods in [2]. A novel current feedback practice for PR control is proposed with a suitable weighted average value of the currents flowing through the feedback current regulator in [3]. The paper [5] minimizes the steady-state error of the grid currents injected in a series mode in the system. A functional switching model for conduction and switching losses in three-phase optimized harmonic stepped-waveform of MLI is developed in [6]. Multilevel Cascaded inverter with the different controllers like PI, PR, and Fuzzy is discussed in [7] for current control. In paper [8] type 2 fuzzy controller is explained .The paper [9] proposes a hybrid single carrier SPWM with a novelty of reduction in switching losses and improved harmonic performance for an inverter. A matrix converter is explained in topology, modulation scheme where look-up tables are avoided for modulation in [10, 11]. A new controller algorithm with a reactive power minimization is proposed in [12]. Fourier analysis of AC to AC converter is synthesized and a generalized transformation of the system is done in [13]. A PI and PR Controller are used for DC output voltage generation using the Space Vector Pulse Width Modulation Technique (SVPWM) in a three-phase Boost rectifier in [14]. A novel and simple control approach to controlling the power injection system (PIS) using state feedback is proposed in [16]. The paper [15, 17] presents a comparison of controller allowed in grid-connected PV to supply stable output with low total harmonic distortion (THD) as per standards under different grid loading conditions. The shortcomings of PI controllers in the form of steady-state error developed PR controllers with the practical application of PR current control for a photovoltaic

(PV) inverter is also described in with advanced PID controller [18, 19]. A control based on the current as a variable for a Z source inverter is proposed in [20]. The Multi-step inverter with different topology is discussed with loss calculation and different carrier modulation methods in [21, 22]. The PR controller with a different variation of the control parameters is discussed in [23] with photovoltaic control and adjustable speed control of ac drives.

In this paper,

- A novel controller technique for the Matrix converter based Cascaded Inverter is proposed with proper tuning of the controller and modeling the same when connected to the three-phase load.
- The controller is customized to control the current with the PR controller and maintain the load power constant at any point of the disturbance, enabling suitable ICF by adjusting the current reference.
- Despite conventional PI regulator, the advantages of the PR controller for current control are discussed for different generated disturbances from the input as well as output side.
- The performances of the controllers are compared in the scale of Fast Fourier Transform (FFT) behavior, assuming constant PV system.

This article is organized by arranging Cascaded H- Bridge Multilevel inverter (CHB-MLI) in Section 2; System Modeling is done in 3. Matrix converter is in section 4 and the algorithm for Matrix converter in 5. Design of controller is in section 6, the System description and Problem Formulation in 7. Waveform and results are shown in section 8 and finally, the conclusion is given in segment 9.

II. Multilevel Inverter (CHB_MLI)

The Inverter with a distinct level contains a cluster of power Silicon-based switches (IGBT) and capacitive voltage sources to skeleton an MLI. The switching sequel adds the strength of each bridge and finally, the output voltage is amplified at the endpoint, thereby reducing the resisting voltage of each switch. The CHB inverter has of an arrangement of single H_bridge units in series, hence accruing the least components as compared to other topologies and cost-effective. [15 and 16]

III. System Modelling

A. The Inverter output voltage Modelling

The voltage level per phase across the load of an MLI is explained by Eqn (1). [2, 6, 21]The delay to the switches is assigned to generate the demanded voltage and emit the minimum order harmonics which is the highest in magnitude among all the harmonic orders. The stretch of the load voltage U_{an} on the basis of Fourier's expansion is in Eqn (2). [4, 15, 21]

$$k=2s-1$$
 (1)

$$U_{an}(wt) = \sum_{x=1,3,5}^{\infty} \frac{4U_{dc}}{x\pi} \left[\frac{\cos ine(h\beta_1)}{+\dots+\cos ine(h\beta_s)} \right] \sin(hwt)$$
(2)

$$U_{an} = \sum_{z=1}^{s} U_{az}$$
⁽³⁾

$$h=3S-k_{x},$$

$$k_{x}=1:S=even, k_{x}=2:S=odd$$
(4)

$$\cos \operatorname{ine}(\beta_{1}) + \dots + \cos \operatorname{ine}(\beta_{S}) = S \times M_{m}$$

$$\cos \operatorname{ine}(x\beta_{1}) + \cos \operatorname{ine}(x\beta_{3}) + \dots + \cos \operatorname{ine}(x\beta_{S}) = 0$$
(5)

Inverter's steps number can be extracted from the Eqn (2) to achieve the optimal angle of switching, (β) of the load potential as in Eqn (3) with the expected bridge number as set in Eqn (4). The Eqn (5) can be used to extract the delay angle using DEA as an iterative technique. [5, 6, 7, 8, 9 and 21]

IV. Matrix Converter

The matrix converter as shown in Fig. 1 is a set of four-quadrant switches connected to three input legs to synthesize one output leg voltage. The common emitter four-quadrant switch configuration is used for simulation of the matrix converter [7, 11, and 15].

Output phases and input phases of the converters are represented as (A, B, C) and (a, b, c). The duty ratio of the three switches is set by a limit as in Eqn (6). The switching cycle average model of voltage and current of input and output phase voltage in terms of duty ratio is given in Eqn (7) and Eqn (8).



Figure.1 represents schematic diagram of Matrix converter

$$O \leq (d_{ab}, d_{bc}, d_{ca}) \leq I, d_{aa} + d_{ba} + d_{ca} = I \qquad (6)$$

$$\begin{bmatrix} U_{an} I_{a} \\ U_{bn}, I_{b} \\ U_{cn} I_{c} \end{bmatrix} = \begin{bmatrix} d_{aa} & d_{ba} & d_{ca} \\ d_{ab} & d_{bb} & d_{cb} \\ d_{ac} & d_{bc} & d_{cc} \end{bmatrix} \begin{bmatrix} U_{a} I_{a} \\ U_{b}, I_{b} \\ U_{c} I_{c} \end{bmatrix} \qquad (7)$$

$$U_{a} (t) = d_{aa} U_{an} + d_{ba} U_{bn} + d_{ca} U_{cn} \qquad (8)$$

$$U_{b} (t) = d_{ab} U_{an} + d_{bb} U_{bn} + d_{cb} U_{cn}$$

$$U_{c} (t) = d_{ac} U_{an} + d_{bc} U_{bn} + d_{cc} U_{cn} \qquad (4)$$

$$d_{aa} (t) = k_{a} \sin(\omega_{i}(t)) \qquad (9)$$

$$d_{ba} (t) = k_{a} \sin(\omega_{i}(t) - 2\pi/3)$$

$$d_{ca} (t) = k_{a} \sin(\omega_{i}(t) - 4\pi/3)$$

$$U_{a} (t) = \frac{3}{2} k_{a} (t) U_{in} \cos(\rho) \qquad (10)$$

$$d_{aaa} = d_{aa} + D_{i} + D_{4} - D_{7}$$
(11)

$$d_{baa} = d_{ba} + D_{2} + D_{5} - D_{8}$$
(11)

$$d_{caa} = d_{ca} + D_{3} + D_{6} - D_{9}$$
(11)

$$D_{1}(t) = \frac{1}{3} + \frac{1}{6} \left[\cos(3\omega_{i}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{2}(t) = \frac{1}{3} + \frac{1}{6} \left[\cos(3\omega_{i}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{3}(t) = \frac{1}{3} + \frac{1}{6} \left[\cos(3\omega_{i}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{3}(t) = \frac{1}{3} + \frac{1}{6} \left[\cos(3\omega_{i}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{3}(t) = \frac{-k_{m}}{6} \left[\cos(3\omega_{i}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{3}(t) = \frac{-k_{m}}{6} \left[\cos(3\omega_{o}t)\cos(\omega_{i}t) \right]$$
(11)

$$D_{5}(t) = \frac{-k_{m}}{6} \left[\cos(3\omega_{o}t)\cos(\omega_{i}t) + 2\pi/3 \right]$$
(11)

Once the delay is set as per Eqn (9), the converter output voltage frequency which is independent of the input voltage frequency as derived in Eqn (10) is developed. [11] To synthesize maximum output magnitude on the output voltage the final duty ratios are developed in Eqn (11).

V. Algorithim of Matrix converter

The algorithm proposed for the converter to achieve the required condition of delay is explained in Fig. 2.



Figure.2 explains the complete Flow Chart of the proposed Algorithm

VI. Controller Design

A. Proportional Resonant Controller (PR)

A Proportional Resonant (PR) is a controlling system with $H_c(s)$ the transfer function in Eqn (12) [15, 16, 23, and 24], which is equivalent to a PI controller in structure. The insertion of supplementary gain can enhance and regulate the frequency to make the system constraints to resonate at the required value. A compensator for harmonics reduction is given in Eqn (13) [9, 11]. G_h , can identify the frequencies close to the resonance without disturbing the spread of the controller. [10] This can be strongly applied in controlling the current with an advantage of synchronization of the current with the disturbances [17, 18, 19 and 20]. The final transfer function can be given in Eqn (14).

$$H_{c}(s) = K_{PI} + K_{II} \frac{s}{s^{2} + \omega^{2}}$$
(12)

$$G_{h} = \sum_{h=3,5,7} K_{th} \frac{s}{s^{2} + (\omega h)^{2}}$$
(13)

$$G_{PR}(s) = \frac{2k_{p1}s + \chi_{p1}}{s^2 + \omega_1^2} + \frac{2k_{p2}s + \chi_{p2}}{s^2 + \omega_2^2} + \frac{2k_{p3}s + \chi_{p3}}{s^2 + \omega_3^2} + \chi_{p4}$$
(14)



Figure.3 (a) explains the bode representation of the PI controller (api1<api2<api3) and Figure.3 (b) explains the bode representation of the PR controller (a1<a2<a3)

From Fig. 3(a) and Fig.3 (b) it can be concluded that the PR controller has a better range of parameter variation due to a wide gain margin as compared to the PI controller. The width of this frequency band (dependent upon the gain margin) depends on the value of proportional constant Kp. Smaller the value of *the constant of proportionality*, wider will be the band. The regulator can be tuned as per the demand depending upon different values of proportional constants.

B. *Mathematical modeling of the system*

The multilevel inverter can be modeled with respect to the switching sequence and the filter components as in Eqn (15, 16, 17 and 18) to tune the controller.

$$\frac{d}{dt}\begin{bmatrix}i_{ab}\\i_{bc}\\i_{ca}\end{bmatrix} = -\frac{r_{L}}{L}\begin{bmatrix}i_{ab}\\i_{bc}\\i_{ca}\end{bmatrix} - \frac{1}{3L}\begin{bmatrix}U_{ab}\\U_{bc}\\U_{ca}\end{bmatrix} + \frac{1}{3L}\begin{bmatrix}sw_{a}-sw_{b}\\sw_{b}-sw_{c}\\sw_{c}-sw_{a}\end{bmatrix}U_{dc}$$
(15)

$$\frac{d}{dt} \begin{bmatrix} \mathbf{U}_{AB} \\ \mathbf{U}_{BC} \\ \mathbf{U}_{CA} \end{bmatrix} = -\frac{1}{\mathbf{R}\mathbf{C}_{f}} \begin{bmatrix} \mathbf{U}_{ab} \\ \mathbf{U}_{bc} \\ \mathbf{U}_{ca} \end{bmatrix} + \frac{1}{\mathbf{C}_{f}} \begin{bmatrix} \mathbf{i}_{ab} \\ \mathbf{i}_{bc} \\ \mathbf{i}_{ca} \end{bmatrix}$$
(16)

(18 $x_1 = i_{ab}, x_2 = ji_{ab}, x_3 = i_{bc}, x_4 = ji_{bc}, x_5 = i_{ca}, x_6 = ji_{ca}, x_7 = U_{ab}, x_8 = jU_{ab}, x_9 = U_{bc}, x_{10} = jU_{bc}, x_{11} = U_{ca}, x_{12} = jU_{ca}$

С. Modeling of the filter The filter parameters of a LC filter can be designed as given in Eqn (19) and Eqn (20).

$$G_{\rm F}(s) = R_{\rm f} + \frac{s^2 L_{f1} C_{\rm f} + l}{s^3 L_{f1} L_{f2} C_{\rm f} + s (L_{f1} + L_{f2})}$$
(19)
$$L_{\rm f} \frac{di}{dt} = U_{\rm dc}, L_{\rm f} = \frac{U_{\rm dc}}{\Delta I^* m_{\rm i}^* f_{\rm sw}},$$
(20)
$$C_{\rm f} = \frac{15\% (MVAR)}{2^* \pi^* f_{\rm sw}^* U^2}, R_{\rm f} = 2\xi \sqrt{\frac{L_{\rm f}}{C_{\rm f}}}$$

D. PR Controller operating in model

The complete PR controller, as given in Fig. 4 takes the set controller parameters with suitable tuning which can be done by linearizing the total system. The combination blocks for the PR controller and complete ICF given in Fig. 5 (a), Fig. 5 (b) and Fig. 5 (c). The final block diagram of a PV integrated three phases MLI with a Matrix converter is shown in Fig. 6.



Figure.4 explains the overall system arrangement to control the load current using PR regulator with respect to





Figure.5 (a) Block diagram of PR regulator

Figure.5 (b) Block diagram of PR regulator with Harmonic content



Figure.5 (c) Block diagram of ICF regulator with PR Regulator Figure.5 explains the overall system arrangement to control the load current using PR regulator



Figure.6 explains the overall system arrangement to control the load current using PR regulator

VII. System description and Problem Formulation of Problem

The proposed system aims in implementing the ICF method in regulating the load current within the range during the disturbance caused from the voltage harmonics distortions (due to the injection of the harmonic current is circulated freely through the filter capacitors present in the inverter as well as matrix converter side) and the load variation. This can be achieved by dragging the inverter current is to zero by a PR controller and resonant harmonic controllers (HCs). The key point to enable control is to generate a reference current (I*) with the information of the harmonic content that can be realized by calculating the capacitor current from the capacitor voltage and then adding it into the reference. The description about the ICF is given in Fig.5 (c). This idea has been incorporated effectively for the ICF control system with a basic objective to get effectuated in the process of integration and achieve low THD.

A three phase five level CHB Inverter is designed in Matlab Simulink environment with the cascaded Matrix converter. The DEA is used to calculate the optimized delay angle as per the predefined objective function as in Eqn (5). The disturbances in the form of PV amplitude change and torque change are introduced and the ICF based PR controller and PI controller are used to find out the Stability.

The detail controller diagram and the system block diagrams are given in section VII.

Assuming each PV generator operating at its MPP, and the load voltage is a function of optimum delay angle, the current in the load side must maintain stability for any generated disturbance. Power factor correction can be achieved with the matrix converter in the input side of the inverter operating with variable frequencies. The total model is operated at a switching frequency of 10 kHz.

VIII. Simulation and Result Analysis

A Matlab Simulink model is developed and the controller is designed in accordance with the block diagram as shown in Fig. 6 with the forethought of the reasonable angle of delaying of MLI and switching sequence of Matrix converter with the parameters of simulation assigned as in Table 1.

System Parameter				
PV voltage	70V			
Matrix output voltage	120V			
Load voltage	100V			
Reference current set	5A			
Load current	<5A			
Matrix conv	erter Parameter			
Km,Km1, p	0-0.57,0-0.22,0.5-1			
Input filter	6mH,1micro F			
Output filter	3mH,10 micro F			
PRR	legulator			
Kpn, χ_P	0-0.5,0-100			
Filter for Inverter				
Input filter	1mH,10micro F			
Output filter	5mH,10 micro F			

TABLE 1Parameters assigned for simulation

Motor Specification Three phase Squirrel Cage Induction Motor			
Nominal Power	300Watt		
Lime voltage	100V		
Frequency	50Hz		
Stator Resistance and Inductance	16.6 ohm,0.0131H		
Rotor Resistance and Inductance	15.3 ohm,0.0131H		
Mutual Inductance	0.825H		
Friction factor	0.001		
Pole pair, Inertia	4 ,0.05kg.m ²		

A. Simulation waveform and result analysis



Figure.7 gives the inverter output voltage after filter



Figure.9 gives the Load voltage with PR controller



Figure.10 gives the Load voltage with the change of frequency with different controller



Figure.11 gives the Load current with the change of frequency with different controller



Figure.12 gives the matrix converter input voltage and current showing power factor improvement where the voltage is scaled down to 12percent with filter.



Figure.13 gives the matrix converter current with PI and PR controller.



Figure.14 gives the Load voltage and current of the system with PR controller with different loads



Figure.15 explains the Load current of the system with PI and PR controller with the voltage sag.



Figure.16 explains the Load voltage of the system with PI and PR controller with voltage sag.



Figure.17 explains the Load current of the system with PI and PR controller.



Figure.18 explains the Load power of the system with PR controller with different loads



Figure.19 explains Inverter power of the system with and without controller and with different controllers The load currents for load variation and input voltage disturbances in the form of PV amplitude change are imported to investigate the attitude of the controller and the responses are presented. The waveforms obtained from simulation are as shown above .They can be summarized to the following points of observations;

1. The simulated output voltage of the inverter per phase as in Fig. 7 after the filter is allowed to pass through the Matrix converter with the calculated filter parameter to give the output voltage of the matrix converter in Fig.8 and Load voltage in Fig.9.

2. The change of frequency is incorporated as a deviation from the set value and the response with different controllers is observed in Fig.10 and Fig.11 in the form of voltage and current signals respectively to justify the superiority of the PR controller.

3. Fig.12 gives a required power factor improvement of the matrix converter when the voltage is scaled down to match the current scale.

4. The matrix converter waveform showing the output current contains lesser spikes with the PR controller in Fig.13 as compared to the PI controller.

5. The three phase load current is varied with the load change in the form of torque variation of the three phase induction motor. The response with the PI and the PR controller is given in Fig.14. From the same figure the smoothness with the PR controller is validated.

6. Voltage sag of 0.8pu is inserted from the input side for a time interval of [0.06s to 0.12s] and response of the system is analyzed with the PR and PI controller. With the insertion of disturbance from the PV side for the interval of [0.06s to 0.12s] in the form of the voltage sag, the voltage and current are showing the comparison with and without the controller in Fig. 15 and Fig. 16.

7. The load current without sag and for a load variation in the form of torque is given in Fig. 17.

8. The load power response is explained in Fig.18 and 19 for different loading using the PR controller and the spikes are reduced with the PR controller.

9. The FFT analysis of the load current is explained in Table 2. From that it is obvious that the PR controller is better in reducing the harmonic.

	5 th		7 th		9 th		11 th	
	PI	PR	PI	PR	PI	PR	PI	PR
Fundamental	5.5	4.9	5.1	4.23	4.8	3.9	2.2	1.9
Fundamental with 5 th compensated	1.1	0.35	4.2	3.85	3.1	2.2	1.5	1.4
Fundamental with 5 th ,7 th compensated	2.3	0.44	0.5	0.86	2.36	2.4	0.5	0.9
Fundamental with 5 th ,7 th ,9 th compensated	1.9	0.37	2.1	0.61	0.43	0.24	0.49	0.4
Fundamental with 5 th ,7 th ,9 th ,11 th compensated	0.5	0.28	0.28	0.39	0.16	0.21	0.1	0.1

 Table2

 FFT list of different harmonic content with different controller

IX. Conclusion

A new current control technique using the concept of ICF is applied in a five-level three-phase CHB multilevel inverter connected to a three phase squirrel cage induction motor load with a matrix converter with the PV system as input. In spite of the limitation of low voltage regulation in the matrix converter, the improved power factor and the naturally commutated ac to ac converter structure dominates the use of the same in the system. The observations made from the simulated waveforms and the analysis obtained from the FFT amplitude can prove the superiority of the PR controller on the PID regulator. The ICF method can able to control the load current within the reference value with appreciable THD reduction is also justified. The design of the filter can be set for more accurate values to reduce the harmonic percentage in the load voltage waveforms and also controller design from the matrix converter side can be considered to be the future scope.

Nomenclature

β	Optimal angle of switching	r_L, r_{cf}	The lumped parameters of filter inductance and capacitance
k _{vgain}	Output voltage gain (maximum).	$U_{a}(t)$	The matrix output voltage in time variant representation
$\beta_1 - \beta_s$	The angles of switching of the IGBT	S	The number of H Bridge of the inverter
G_h	The compensator harmonic transfer function	h	The number of order of harmonic
Ę	The filter coefficient	ρ	The offset duty ratio correction factor
L_f, C_f, R_f	The filter inductance, capacitance and resistance	G _{PR} (s)	The overall Controller transfer function
D ₁ -D ₇	The final delay angles of Matrix converter	I _a	The phase current
K_{th}, K_{Pl}, K_{Il}	The gain of the compensator.	* I	The Reference current
$\chi_{P1,}k_{p1}$	The gain of the compensator.	d _{aa}	The switching delay of the first switch first arm of the converter
Km, km1	The improvement constants	\mathbf{f}_{sw}	The switching frequency
M _m	The index required for modulation	$H_{c}(s)$	The transfer function of PR controller

U _{az}	The individual source voltage for S number of sources	G _F (s)	The transfer function of the filter
U _{an}	The Inverter phase voltage	U _{dc}	Voltage of the dc capacitor
R,L	The load components		

X. References

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