Rectifilter for electric arc plasma plant

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Abstract: This article presents the results of the development and research of a new type of AC-DC converter - a filterrectifier (or rectifilter). The object of application of the proposed device is an industrial plant of electric arc plasma processing, the operation of which requires maintaining a constant value of the arc current and is accompanied by a high level of harmonic distortion and consumed reactive energy. From the point of view of the reactive power compensation and harmonic filtering strategy, the rectifilter can be attributed to active electric power filters and FACTS, however, from the position of the main function performed, it is a direct current source, an AC to DC converter, an active rectifier. This work describes in detail the principles of operation of the control system and the hardware architecture of the device. Mathematical modeling of the rectifilter and analysis of the graphs of the consumed alternating current, rectified current, analysis of the harmonic composition and power factor in comparison with the thyristor rectifier of an electric arc plasmatron were carried out.

Keywords: electric arc; plasmatron; energy efficiency; FACTS; active power filter; active rectifier; current source

1. Introduction

Existing energy-intensive electrotechnological plants in most cases have in their structure a converter that plays the role of a voltage source. For such solutions, methods have been developed that maximize the efficiency of energy consumption (3, 5, 28). In processes and plants that require maintaining a predetermined current value, such as an electric arc plasma plant, the operation of the converter as a current source is required (1, 2, 13, 18, 27). At the same time, the existing energy conversion systems significantly worsen the quality of the supplied electric energy, which leads to an increase in the cost of using technological equipment, a decrease in the stability of the power supply network, and a decrease in the resource of the equipment (8, 26).

In the matter of increasing the efficiency of energy consumption using FACTS, the most important tasks are to ensure the best operation speed and accuracy when restoring the required waveform of current or voltage (23, 30). This is largely due to the fact that in the process of modernizing technological complexes, electronic and electrical systems are often used that introduce additional harmonic distortions and reactive energy flows into the power supply network (20). Increasing the range and level of harmonic distortion steadily reduces the efficiency of most FACTS (7, 22). In addition, being mainly an element of the power supply network on the transmission side, they are not able to reduce the loss of electricity in equipment and the elements on the consumer side. Thus, the most promising solution to improve the energy efficiency of large technological plants is to install FACTS on the consumption side and control energy flows taking into account the process flow, and, as a consequence, the modes of electricity consumption.

There are solutions that allow you to adapt the FACTS hardware architecture for specific technological plants, as well as solutions for joint control of the technological process (16) and the process of restoring the waveform of the consumed current. Considering the class of adapted systems for active energy filtering (AAEF), it should be noted that their adaptation at the level of hardware solutions can only be realized with rectifiers built according to the topology of the voltage source (10, 11, 14, 15, 17, 21, 24). At the same time, for the considered systems of plasma electric arc processing, the topology of the rectifier in the form of a current source is required. Therefore, the first requirement for AAEF in the complex of an electric arc plasmatron will be the ability to restore the current waveform at the point of connection of the current source. In addition, when developing AAEF for an electric arc plasmatron, it is necessary to take into account changes in the technological process. The application of this adaptation approach leads to a significant increase in the overall dimensions of the electrical complex, since, in addition to the existing plasmatron converter, the installation of an AAEF converter will be required. It follows that the most promising from the point of view of technical and economic efficiency will be the adapted electric power filtration systems installed in or instead of the plasmatron converter (12, 19, 29) which can be defined as the second requirement for the developed system.

The proposed solution for constructing a harmonic filtering system and reactive power compensation is as follows. Instead of a controlled rectifier on single-operation thyristors of the plasmatron converter, a rectifier should be installed on fully controlled semiconductor switches with a maximum switching frequency exceeding the order of distorting harmonics. Such a rectifier, on the one hand, will play the role of a current source for the formation and maintenance of the electric arc, on the other hand, it will form the effective consumption of electric current. Based on the value of the required electric arc current, as well as the instantaneous values of currents and voltages in phases at the connection point, the rectifier control system will set the waveform of the consumed current. Ensuring operation speed can also be achieved by optimizing the algorithms for generating control pulses of the control system.

2. Experimental plant

2.1. Hardware

The rectifier of the experimental plant is a single-level three-phase bridge rectifier on IGBTs with blocking diodes. The schematic diagram of the power unit is shown in Fig. 1.

On IGBT gates provide switching signals from the control system. The control system receives:

- measured instantaneous values of phase currents consumed from the main and line voltages at the connection point of the plant (from the measuring unit MB),

- measured current values in a DC circuit (from CS current sensor),

- the measured values of the potential difference between the anode and cathode (from the voltage sensor VS).



Fig. 1. Schematic diagram of the power part of the experimental plant

The plasmatron rectifier is connected to the main through the reactor blocks L1 - L3 and linearly connected capacitors C1 - C3. They are installed to reduce, respectively, ripple current and voltage. VD1-VD6 diodes are required to block reverse diodes in the IGBTs VT1-VT6. In addition to the CS current sensor, a choke L4 is included in the DC link, which serves to accumulate energy and maintain a constant direct current value. The circuit, consisting of the reverse diode VD7 and resistor R, serves to discharge the inductance, and protects the converter from overvoltage at the time of breaking the DC circuit. The anode of the electric arc plasmatron is connected to the positive contact of the converter, and the cathode – to the negative.

2.2. Control System

The essence of the proposed control method is to determine the waveforms of the consumed phase currents, which, on the one hand, will provide the required value of the arc current in the DC link of the converter, and on the other hand, will correspond in harmonic composition and phase shifts to the waveforms of the corresponding phase voltages. The functional diagram of the rectifilter control system is shown in Fig. 2.

The operation of the control system is as follows.

Based on considerations of simplifying the logic of the formation of control pulses, we will limit the number of combinations N = 6 so that three of them and their corresponding inverse combinations commute exclusively two phases at the input of the converter. Thus, the state Q of the converter switches will be determined by the following system of equations:



Fig. 2. Functional diagram of the control system of the experimental plant \mathbf{F}

$$Q = \begin{cases} \begin{bmatrix} 1 & 0 & 0 & 1 & 0 & 0 \end{bmatrix}, \text{ if } \Delta i_{AB}(t) \ge h/2; \\ \begin{bmatrix} 0 & 1 & 1 & 0 & 0 & 0 \end{bmatrix}, \text{ if } \Delta i_{AB}(t) < -h/2; \\ \begin{bmatrix} 0 & 0 & 1 & 0 & 0 & 1 \end{bmatrix}, \text{ if } \Delta i_{BC}(t) \ge h/2; \\ \begin{bmatrix} 0 & 0 & 0 & 1 & 1 & 0 \end{bmatrix}, \text{ if } \Delta i_{BC}(t) < -h/2; \\ \begin{bmatrix} 0 & 1 & 0 & 0 & 1 & 0 \end{bmatrix}, \text{ if } \Delta i_{CA}(t) \ge h/2; \\ \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}, \text{ if } \Delta i_{CA}(t) \ge h/2; \end{cases}$$

where $\Delta i_{AB}(t), \Delta i_{BC}(t), \Delta i_{CA}(t)$ is the differences between calculated and reference hypothetical linear currents, [A]. System of these currents $\Delta i_{line}(t) = \{\Delta i_{AB}(t); \Delta i_{BC}(t); \Delta i_{CA}(t)\};$

h - the width of the hysteresis zone of the hysteresis controller HC, [A]. It is usually set at 5 \div 10% of the nominal consumed phase current.

This system of equations is presented in the control system in the form of a switching combinations table SCT and hysteresis controllers HC.

The difference is determined, as explained above, by the following equation

$$\Delta i_{line}(t) = i^*_{line}(t) - i_{line}(t),$$

where $\vec{i}_{line}^{*}(t) = \left\{ \vec{i}_{AB}^{*}(t); \vec{i}_{BC}^{*}(t); \vec{i}_{CA}^{*}(t) \right\}$ is the system of reference hypothetical linear currents, [A]; $i_{line}(t) = \{i_{AB}(t); i_{BC}(t); i_{CA}(t)\}$ - system of calculated hypothetical linear currents, [A].

Calculation of hypothetical linear currents $i_{line}(t)$ is carried out by phase-linear transformations of the measured phase currents $i_{phase}(t) = \{i_A(t); i_B(t); i_C(t)\}$:

$$\begin{cases} i_{AB}(t) = i_A(t) - i_B(t); \\ i_{BC}(t) = i_B(t) - i_C(t); \\ i_{CA}(t) = i_C(t) - i_A(t). \end{cases}$$

Setting of hypothetical linear currents has the following form:

$$\begin{cases} i_{AB}^{*}(t) = I_{amp}^{*} \cdot w_{AB}(t) \\ i_{BC}^{*}(t) = I_{amp}^{*} \cdot w_{BC}(t) \\ i_{CA}^{*}(t) = I_{amp}^{*} \cdot w_{CA}(t), \end{cases}$$

where $W_{AB}(t), W_{BC}(t), W_{CA}(t)$ - reference waveforms of currents of unit amplitude, dimensionless quantity. System of these signals $w_{line}(t) = \{w_{AB}(t); w_{BC}(t); w_{CA}(t)\};$

 I_{amp}^{*} - the scaled amplitude of the reference hypothetical linear currents, [A]. This value is obtained by the following ratio:

$$I_{amp}^* = \sqrt{3} \cdot I_{PI}^*,$$

where I_{PI}^{*} is the output signal of the PI controller, [A].

The PI controller input receives the values of the reference arc current I_{arc}^* , [A] and the actual value of the arc current, I_{arc} [A].

$$w_{line}(t) = \frac{v_{line}(t)}{V_{m \, line}},$$

where $v_{line}(t) = \{v_{AB}(t); v_{BC}(t); v_{CA}(t)\}$ is the system of measured linear voltages, [V].

 $V_{mline} = \{V_{mAB}; V_{mBC}; V_{mCA}\}$ - the system of amplitudes of the measured linear voltages, [V], which is determined using the tools of the fast Fourier transform (FFT) by the ratio:

$$V_{m \, line} = \frac{2\sqrt{2}}{T} \sqrt{\left(A\right)^2 + \left(B\right)^2}.$$

where $A = \int_{t-T}^{t} v_{line}(t) \cos(2\pi f t) dt$, $B = \int_{t-T}^{t} v_{line}(t) \sin(2\pi f t) dt$,

T - period of fundamental harmonic, [c],

 $f = T^{-1}$ - fundamental harmonic frequency, [Hz].

One of the important elements of the proposed control system is also a prioritizer, the operation of which is determined by the priority calculation algorithm (PCA) and consists in controlling the signal transmission on each of the IGBT control lines depending on the level of harmonic distortion and the magnitude of the phase shift of phase currents. This algorithm allows to solve the problem of the appearance of additional distortions in the waveform of the consumed current during regulation.

3. Modelling Results

The operation of the proposed system was simulated in MATLAB Simulink. Fig. 3a shows graphs of the phase voltage u_a and the consumed phase current i_a during the operation of the rectifilter without a prioritizer. As can be seen from the phase current graph, such a rectifilter operation leads to additional distortions in the current waveform. These distortions appear in cases when the control system simultaneously tries to fulfill several conditions for compensation of linear currents. The result of the prioritizer operation reflects the current graph in Fig. 3b. As can be seen, the waveform of the phase current i_a repeats the waveform of the phase voltage u_a without distortions.

voltage u_a without distortion.



Fig. 3. Graphs of phase A current and phase A voltage:

a - without prioritizer, b - with prioritizer

With the hysteresis width $h = 0.05 \cdot I_{arc}$, the total level of harmonic distortion of the phase currents after the saturation process of the DC inductor is: $THD_I \leq 1.13$ %. The level of harmonic distortion of the current when using a thyristor AC/DC converter of the electric arc plasmatron is $THD_I = 8.3 \div 38.5$ %. The power factor of the rectifilter is PF = 1; thyristor converter is $PF = 0.43 \div 0.86$. The function of maintaining a given level of arc current is confirmed by the graphs in Fig. 4. From the graphs of the current it can be seen that the actual value of the arc current corresponds to the reference value.



4. Conclusions

Making a conclusions about the originality of the proposed solution, one should pay attention to a similar class of devices - active rectifiers AFE (9), which have a voltage inverter topology and are not suitable for systems powered by a current source. There are solutions that allow the use of this type of rectifiers as current sources (6, 25), however, the use of direct current at the output of the rectifier as a target parameter of the control algorithm leads to a high level (relative to active power filtering systems) of harmonic distortions of the consumed current and does not allow control reactive energy flows. In view of the use of instantaneous values of phase currents at the input of the converter as a target parameter of the control algorithm in the proposed solution, such a converter should be compared with flexible ac-transmission systems. When comparing the control systems of the filter-rectifier and active power filters, it should be noted that in the latest solutions using the topology of the current source (4), the PQ method is used as an algorithm for determining the reference values of the recovered phase current, based on calculating the power averaged over the period and filtration, and characterized by lower performance in comparison with the proposed method. Also, in such a solution (4), PWM modulation is taken as the main method for generating control pulses, which in this topology differs in significant voltage ripples on the AC side, rather than when using hysteresis regulators used to control rectifier filters.

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